

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: § Patent No.: 6919258
GRANT, JOHN M., et al. §
Application No.: 10/677573 § Issue Date: 7/19/2005 12:00:00 AM
Filed: 10/2/2003 12:00:00 AM § Examiner: Minh-Loan Tran
Docket No.: SC12480TP § Group Art Unit: 2826
Title: SEMICONDUCTOR DEVICE INCORPORATING A DEFECT CONTROLLED
STRAINED CHANNEL STRUCTURE AND METHOD OF MAKING THE SAME

Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

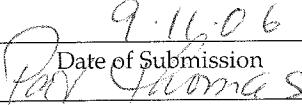
Addressed per C.F.R.§ 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.

Facsimile transmitted in accordance with C.F.R.§1.6(d).

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9/16/06

Date of Submission


Signature

Pat Thomas

Printed Name of Person Signing Certificate

Commissioner for Patents
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter these corrections. Since the errors appear to be on the part of the United States Patent Office, there should be no charge.

9/11/2006
Date

Respectfully submitted,


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CERTIFICATE OF CORRECTION

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PATENT NO.: 6919258
APPLICATION NO: 10/677573
DATE: 7/19/2005 12:00:00 AM
INVENTOR(S): GRANT, JOHN M.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 10, Line 58, Claim No. 6:

Delete "equal to or".

In Column 12, Line 24, Claim 9:

Change "overflying" to - -overlying- -

MAILING ADDRESS OF SENDER (Please do not use customer number below)

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